

F-068

**Analog low-pass filter with double sampling**

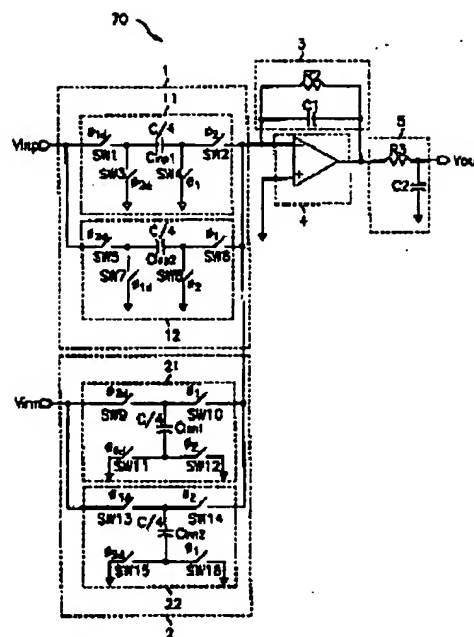
**Patent number:** FR2765417  
**Publication date:** 1998-12-31  
**Inventor:** SHIN YUN TAE  
**Applicant:** HYUNDAI ELECTRONICS IND (KR)  
**Classification:**  
- **International:** H03H11/02  
- **European:** H03H19/00B  
**Application number:** FR19980008317 19980630  
**Priority number(s):** KR19970029634 19970630

**Also published as:**

US6107871 (A1)  
KR256251 (B1)  
JP11168353 (A)

**Abstract of FR2765417**

The filter (70) includes two terminals (1,2) which receive differential analog signals ( $V_{inp}$ ,  $V_{inn}$ ) which charge / discharge four capacitors ( $C_{inn1}$ ,  $C_{inn2}$ ,  $C_{inp1}$ ,  $C_{inp2}$ ). A differential amplifier (4) is connected to the common terminal of the capacitors. A filtering module (5) is connected between the amplifier asynchronous output and the filter output terminal. Switches (SW1SW8) control the charge /discharge sequence of the capacitors. These switches are controlled by a clock signal.



Data supplied from the esp@cenet database - Worldwide